

FIG.1

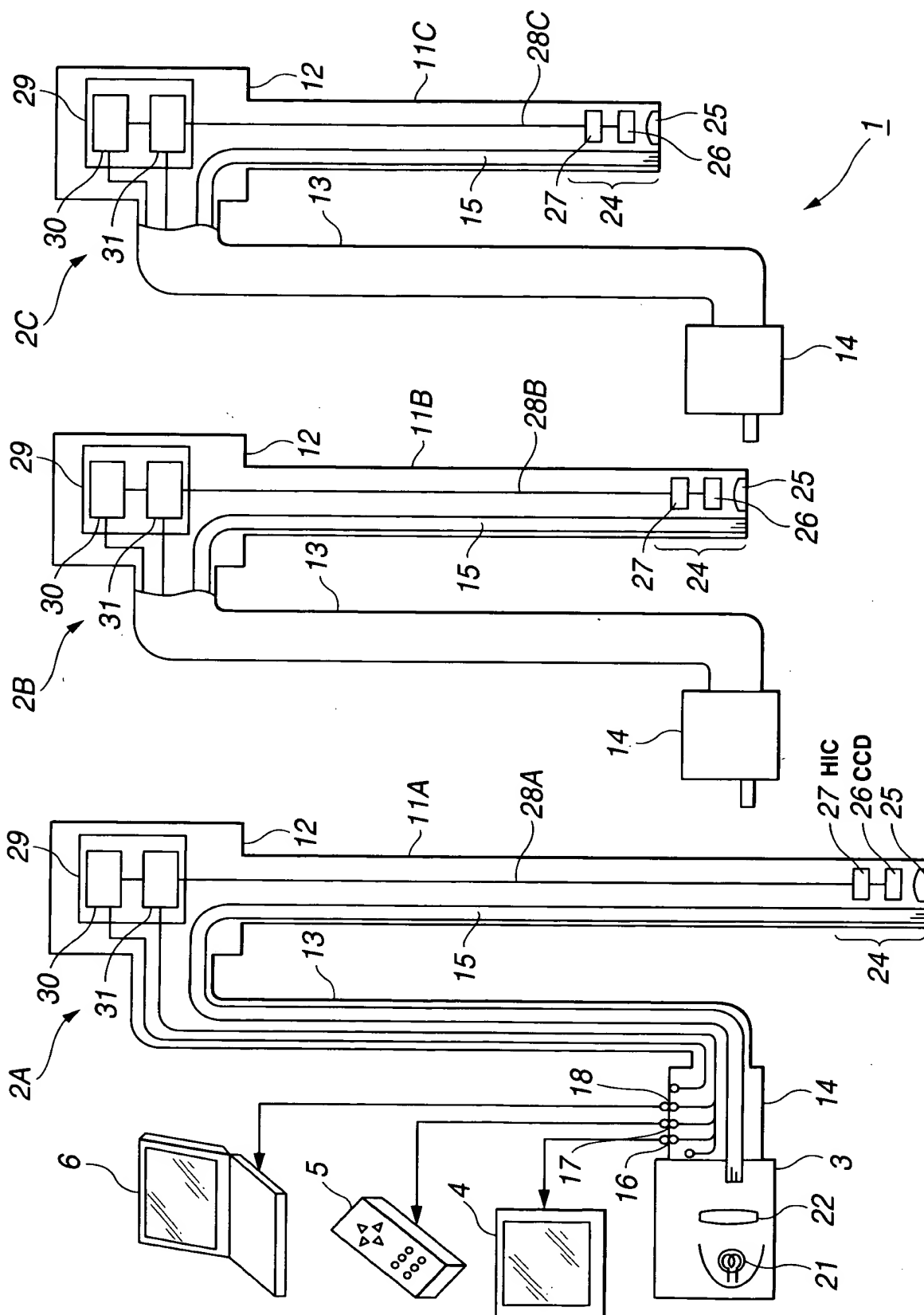


FIG.2

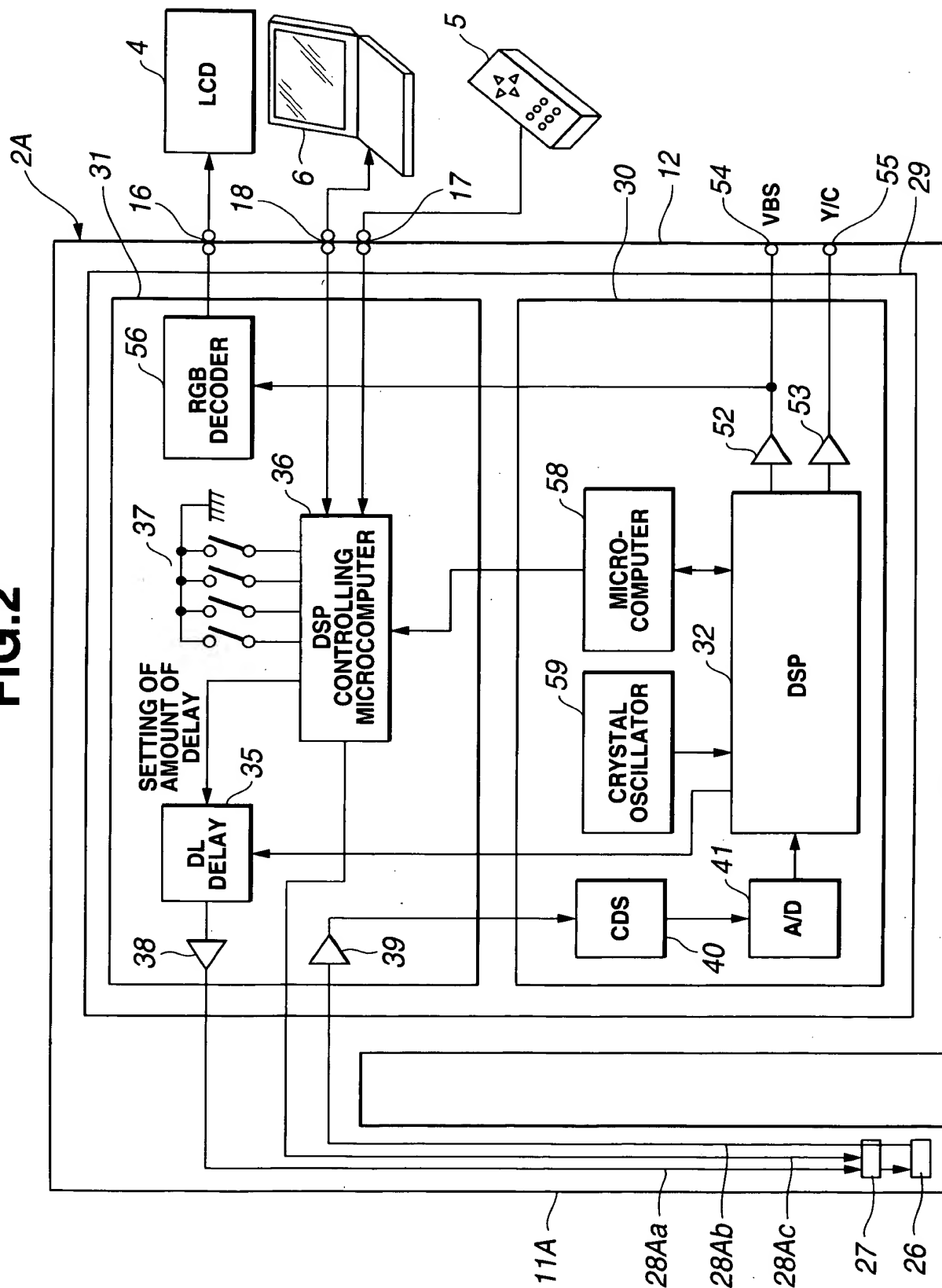


FIG.3

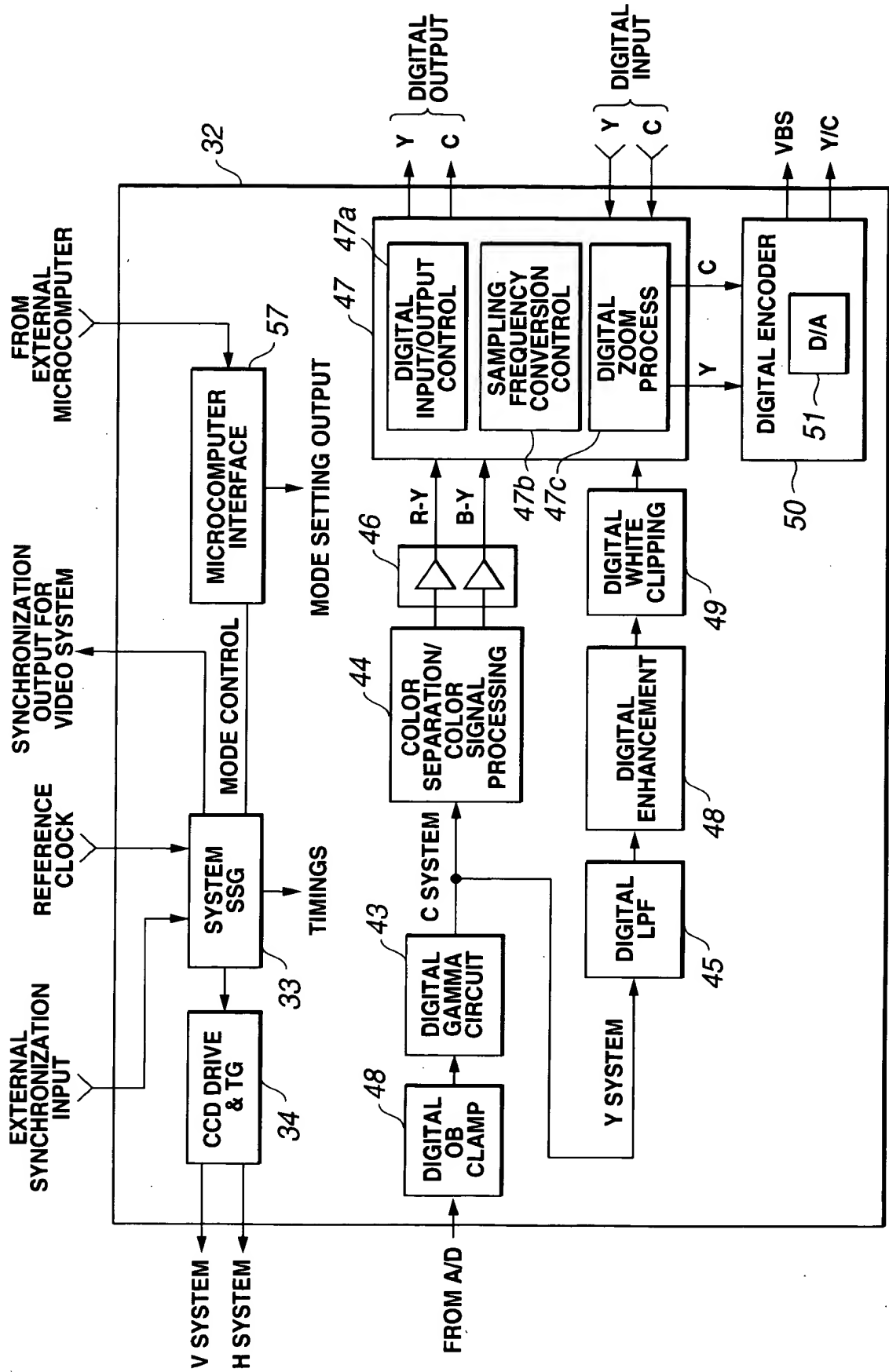


FIG.4

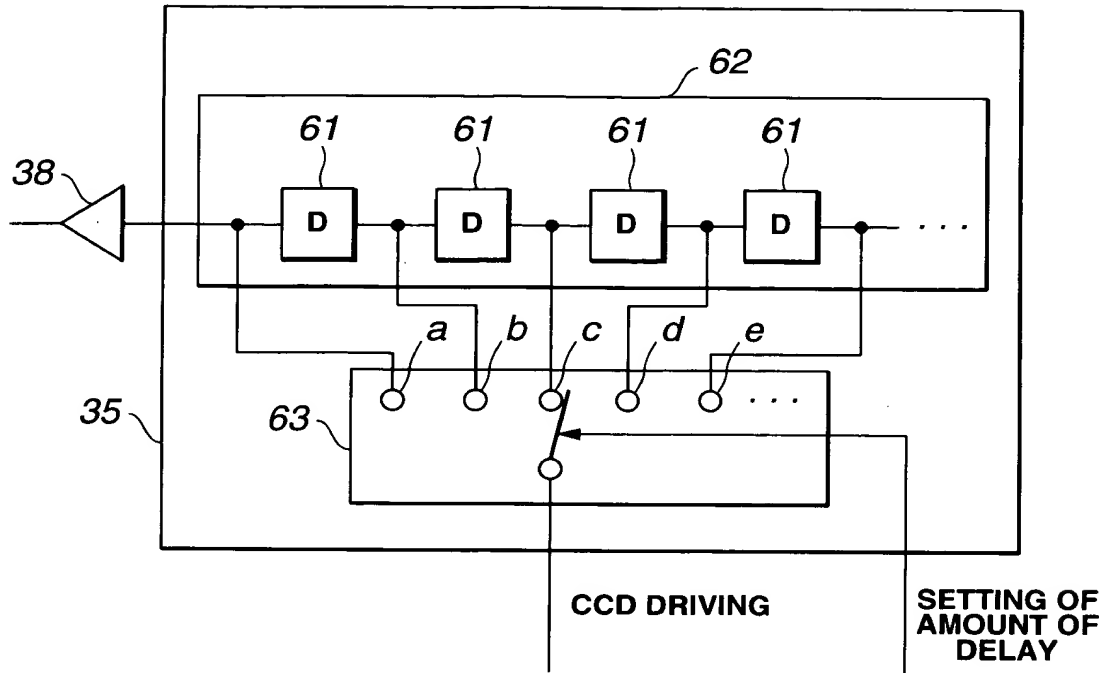


FIG.5A ϕ_H
(NO DELAY)



FIG.5B ϕ_H
(LONG SIGNAL
LINE LENGTH)



FIG.5C ϕ_H
(MEDIUM SIGNAL
LINE LENGTH)



FIG.5D ϕ_H
(SHORT SIGNAL
LINE LENGTH)



FIG.6

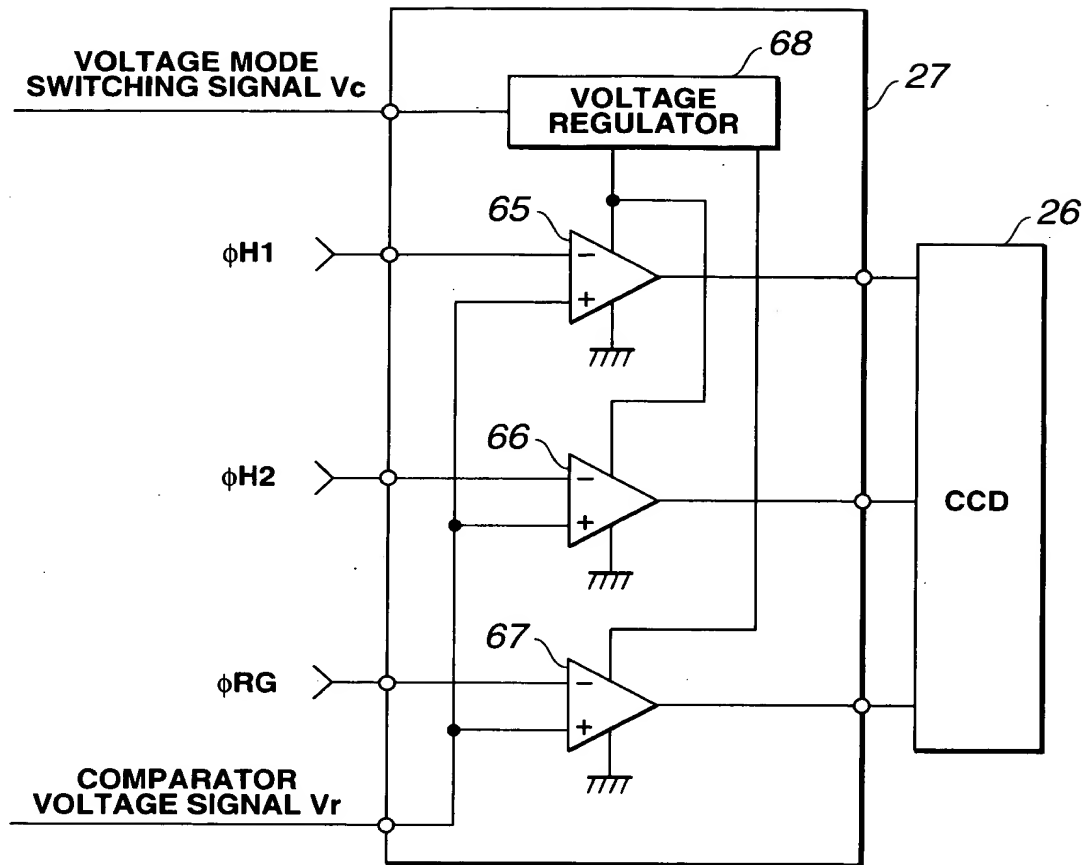


FIG.7A

INPUT
SIGNAL ϕRG

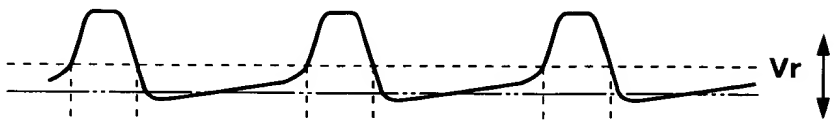


FIG.7B

OUTPUT
SIGNAL ϕRG

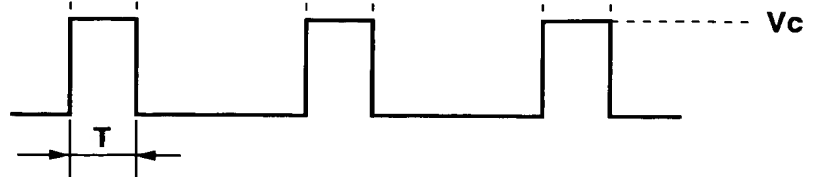


FIG. 8

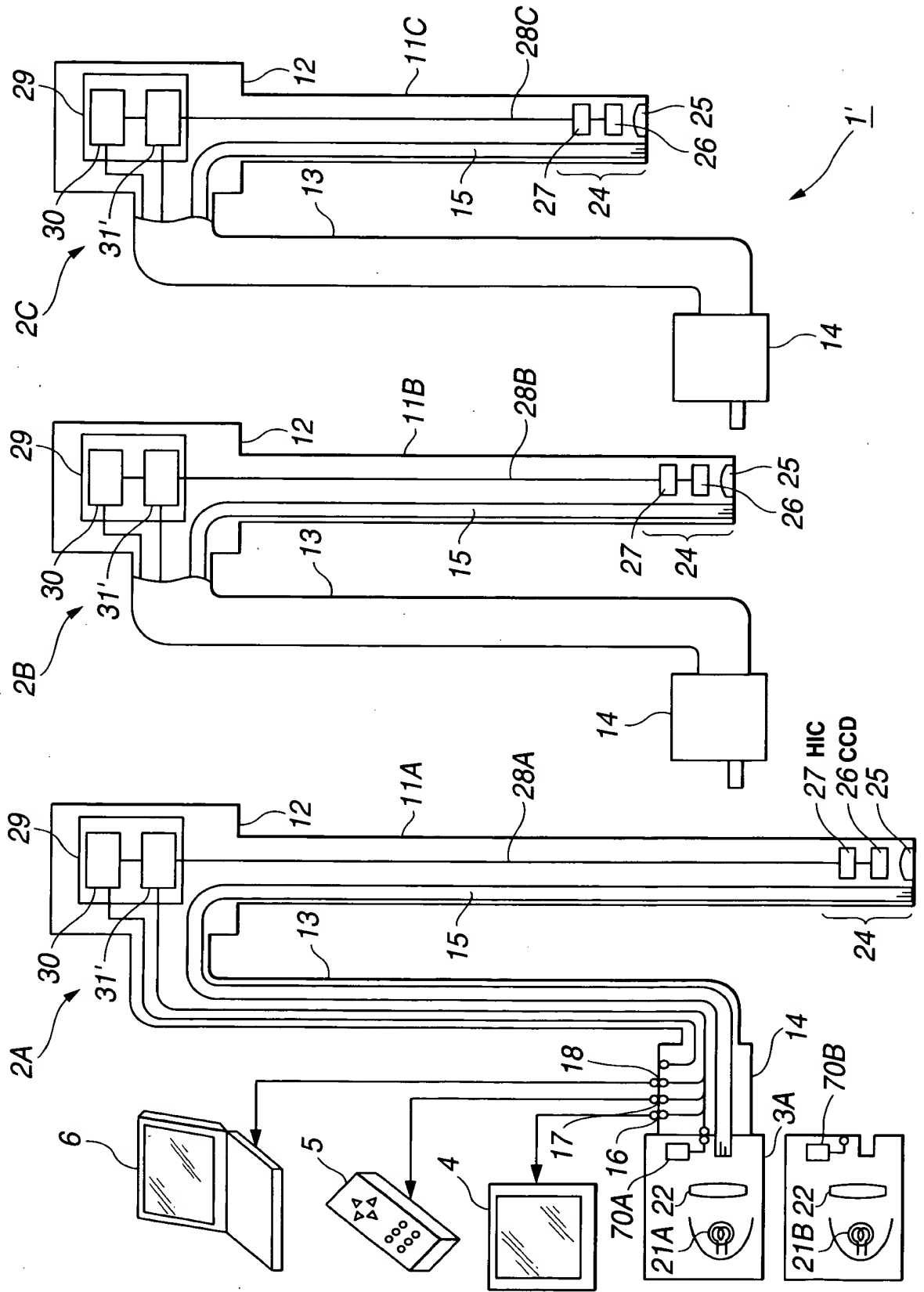
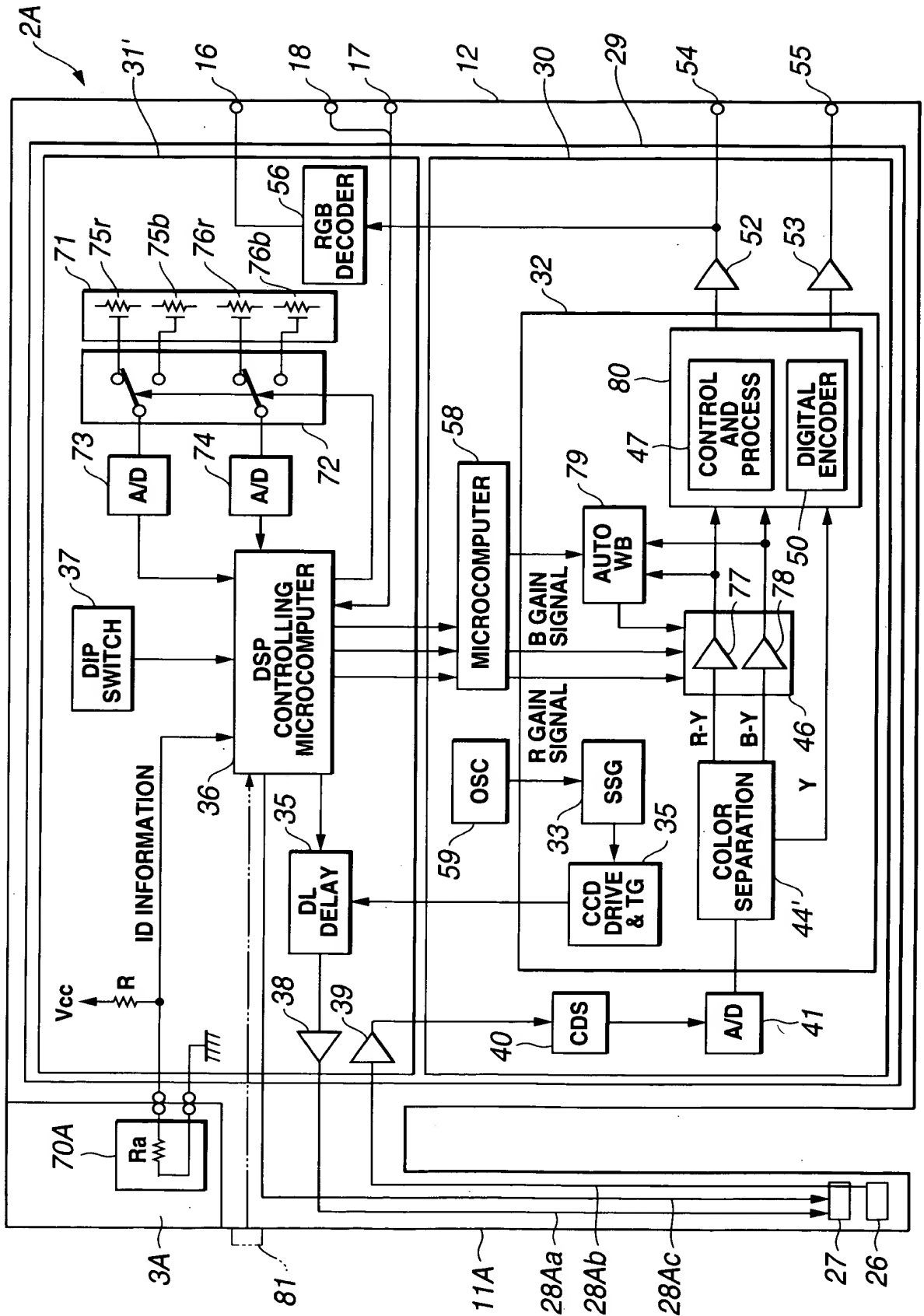


FIG.9



| | | |
|-----------|-----------|----------|
| APPROVED | O.G. FIG. | |
| BY | CLASS | SUBCLASS |
| DRAFTSMAN | | |

FIG.10

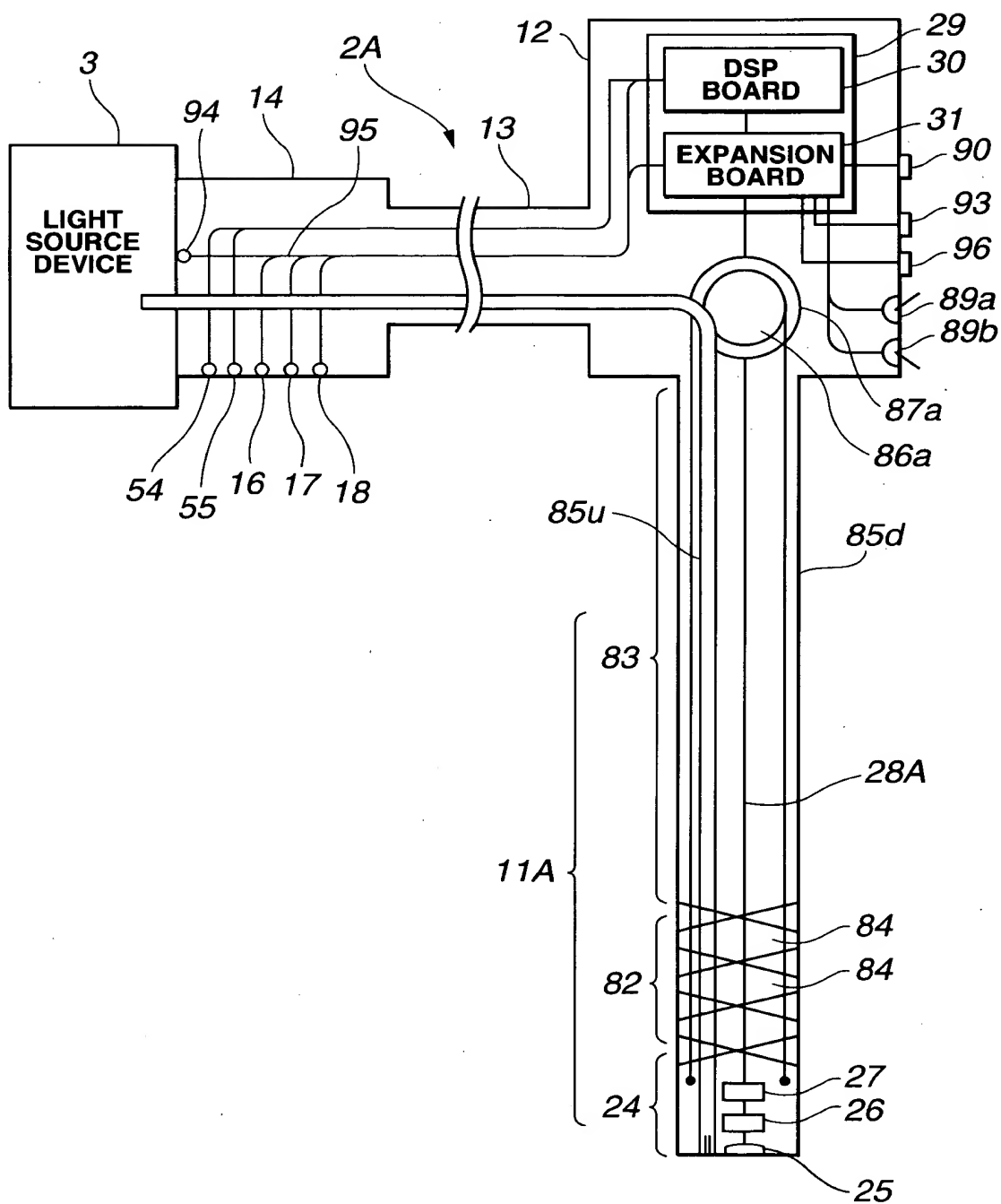


FIG.11

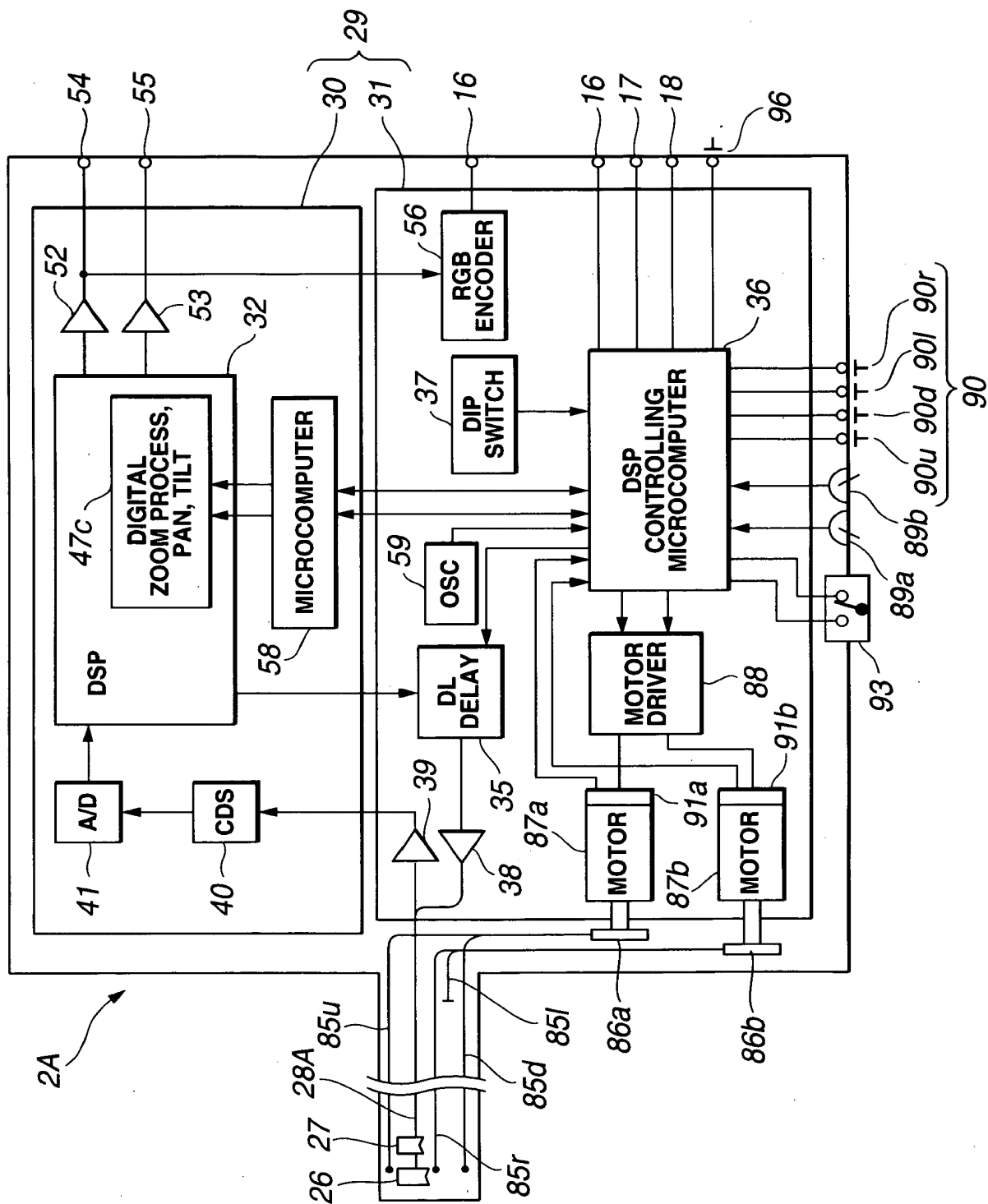


FIG. 13

